



RECEIPT

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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Shigeru SEMBONMATSU, et al.

Appln. No.: 09/559,348

Group Art Unit: 3728

Filed: April 27, 2000

Examiner: Unknown

For: TRAY FOR SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

REQUEST FOR CORRECTED OFFICIAL FILING RECEIPT

Assistant Commissioner for Patents
Office of Initial Patent Examination
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Washington, D.C. 20231

Sir:

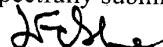
We enclose a copy of the Official Filing Receipt for the above-identified application and request the following correction:

Change the second Inventor's address to read --Chiba, Japan--.

Verification for the requested correction is indicated on the original Declaration filed April 27, 2000.

This error was caused by the PTO and therefore no fee is necessary.

Respectfully submitted,



J. Frank Osha
Registration No. 24,625

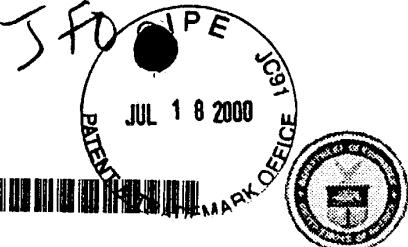
SUGHRUE, MION, ZINN,
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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY.DOCKET.NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/559,348	04/27/2000	3728	690-	Q059017-	9-	9-	1-

Sughrue Mion Zinn
Macpeak & Seas PLLC
2100 Pennsylvania Avenue NW
Washington, DC 20037-3202

Date Mailed: 06/30/2000

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the PTO processes the reply to the Notice, the PTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Shigeru Sembonmatsu, Tokyo, JAPAN;
Manabu Ishikawa, Ichihara-shi, JAPAN;
Chiba, JAPAN

Continuing Data as Claimed by Applicant

Foreign Applications

JAPAN 124326/1999 04/30/1999

If Required, Foreign Filing License Granted 06/29/2000

Title

Tray for semiconductor integrated circuit device

Preliminary Class

206

Data entry by : WYATT, DEBRA

Team : OIPE

Date: 06/30/2000



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Title 37, Code of Federal Regulations, 5.11 & 5.15**



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Bib Data Sheet



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Address: COMMISSIONER OF PATENTS AND TRADEMARKS
Washington, D.C. 20231

SERIAL NUMBER 09/559,348	FILING DATE 04/27/2000 RULE	CLASS 206	GROUP ART UNIT 3728	ATTORNEY DOCKET NO. Q059017
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APPLICANTS

Shigeru Sembonmatsu, Tokyo, JAPAN;
Manabu Ishikawa, CHIBA, JAPAN;

** CONTINUING DATA *****

** FOREIGN APPLICATIONS *****

JAPAN 124326/1999 04/30/1999

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 06/29/2000

Foreign Priority claimed	<input type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 9	TOTAL CLAIMS 9	INDEPENDENT CLAIMS 1
35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				

ACKNOWLEDGED

Examiner's Signature _____ Initials _____

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TITLE

Tray for semiconductor integrated circuit device

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